FACULTY MENTOR  Bott-Suzuki, Simon

PROJECT TITLE  A multiple output digital delay module for diagnostic timing

PROJECT DESCRIPTION
Our group carries out fast (ns to us) timescale experiments using high power electrical pulses, and require specialized digital delay generators (DDG) to trigger at voltage from 5 V to 500 V and possibly using optical trigger systems too. The selected intern(s) will help design, develop and implement a new DDG module to support new diagnostic capabilities in the laboratory.

INTERNS NEEDED  2 MS Students OR 2 Undergrad Students

PREREQUISITES
Knowledge and/or experience with digital output systems, (TTL or similar circuit timing outputs).