FACULTY MENTOR
Drew Hall

PROJECT TITLE
Analog IC Block Design

PROJECT DESCRIPTION
Design of analog IP blocks (OpAmps, Comparators, References, etc.)

This project will be in person.

INTERNS NEEDED
➢ 2

PREREQUISITES
➢ ECE 164 and preferably 264A.
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PROJECT TITLE
Automating Circuit Design

PROJECT DESCRIPTION
Using Python and/or Verilog, automate various aspects of analog integrated circuit design (e.g., padframe, SPI, etc.).

This project will be in person.

INTERNS NEEDED
➢ 2

PREREQUISITES
➢ ECE 111 and Python experience.