FACULTY MENTOR
Kenji Nomura

PROJECT TITLE
Development of Oxide Logic Circuits

PROJECT DESCRIPTION
Fabrication and characterization of logic circuits using oxide TFT.

This project will be in person.

INTERNS NEEDED
➢ 2

PREREQUISITES
➢ None
FACULTY MENTOR
Kenji Nomura

PROJECT TITLE
Development of New Oxide Semiconductor for TFT Applications

PROJECT DESCRIPTION
Fabrication and characterization of oxide semiconductor for TFT application.

This project will be in person.

INTERNS NEEDED
➢ 2

PREREQUISITES
➢ None
FACULTY MENTOR
Kenji Nomura

PROJECT TITLE
Process Development for Oxide-TFT

PROJECT DESCRIPTION
Energy-efficient and low-cost process development for oxide-TFT.

This project will be in person.

INTERNS NEEDED
➢ 2

PREREQUISITES
➢ None