FACULTY MENTOR
Drew Hall

PROJECT TITLE
IC IP Design

PROJECT DESCRIPTION
Design of IP blocks (amplifiers, references, data converters, etc.) in TSMC 65 nm.

This project will be in person.

INTERNS NEEDED
2 Students

PREREQUISITES
- ECE 164
- ECE 264A, B
FACULTY MENTOR
Drew Hall

PROJECT TITLE
Hardware Test Engineer

PROJECT DESCRIPTION
Design PCBs, automate testing using Python for instrument control, and writing FPGA code.

This project will be in person.

INTERNS NEEDED
2 Students

PREREQUISITES
- ECE 164
- ECE 165 (or 260)
- Experience designing PCBs